



IN THE UNITED STATES PATENT AND TRADEMARK

Appl. No. : 10/718,608 Confirmation No. 1693
Applicant : H. MIZUNO et al
Filed : November 24, 2003
Titled : A SEMICONDUCTOR INTEGRATED CIRCUIT HAVING
OSCILLATORS OR OSCILLATION CIRCUITS CONNECTED
TO A WIRING LINE AT CONNECTION POINTS WITH
INTERVALS IN LENGTH THEREBETWEEN (As Amended)
TC/A.U. : 2817
Examiner : A. Kinhead
Docket No.: H-702-02
Customer No.: 24956

Commissioner for Patents
PO Box 1450
Alexandria VA 22313-1450

RESPONSE

Sir:

In response to the Office Action of September 9, 2004
Applicants respond as follows. A Petition and fee for a one-
month Extension of Time also accompanies this response.

Amendments to the Abstract begin on page 2 of this paper.

Remarks begin on page 4 of this paper.